



## ÓSCAR GÓMEZ FUENTE

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### PROFESSIONAL PROFILE

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I have a degree in Physical Sciences and Electronics Engineer. I have been working on electronics for 18 years with experience in leading projects.

Throughout my professional career, I have worked on different hardware platforms: FPGAs, microprocessors, microcontrollers and M2M (Machine to Machine) modules. I have experience with different technologies: PMICs, power switches, batteries, battery chargers, fuel gauges, ADCs, DACs, IMUs, I2C, SPI, NFC, USB, UART, PoE and Image Sensors. As well I have experience managing laboratory devices: oscilloscopes, power supplies, signal generators, DC power analyzers, RF spectrum analyzers and VNAs (Vector Network Analyzers).

I have used different hardware design tools, although I have been working with Altium Designer for the 10 last years. I have specialized in electronic design techniques for compliance with EMC (Electromagnetic Compatibility), with the goal of obtaining the CE Marking.

Besides, I have experience in developing optimized embedded Linux systems using buildroot for different hardware platforms, as well as developing small drivers in the Linux kernel, for the control of different peripherals.

During the last years, I have been working on IoT (Internet Of Things) devices and I have specialized in developing products with different LPWAN (Low Power Wide Area Network) technologies, for example, Sigfox and NB-IoT. I have been the product manager getting involved in all the design steps: firmware, electronic design, envelope design, adaptation antenna and certifications (Sigfox Ready and CE Marking).

On my website you can complete this information: [www.oscargomezf.com](http://www.oscargomezf.com)

### EXPERIENCE

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#### TST Sistemas [Celestia Technologies Group]

03/2021 – Now

Director of Engineering | Santander

TST Sistemas is an expert company developing IoT products with an important background, knowledge and experience in LPWAN Technologies, therefore it is the perfect partner for helping you out in developing your new IoT product, as well as everything related to electronics.

After three years working as IoT Proyecto Manager in TST Sistemas, I was promoted to Director of Engineering.

#### TST Sistemas [Celestia Technologies Group]

03/2018 – 02/2021

IoT Project Manager | Santander

Project manager and Manager responsible for the hardware. Due to my varied experience in different kind of projects, I am able to manage hardware and firmware designs: electronic system design, schematics and PCB design, as well as embedded system design.

My tasks as a project manager are: Manage project requirements, keep the project organized, create and assign tasks, track budgets and motivate the team.

#### Celestia Technologies Group

08/2017 - 02/2018

R&D Electronic Engineer | Santander

After the absorption of Tedesys by Celestia Technologies Group, I continued with my tasks and R&D projects pending completion.

## **Tedesys Global [Celestia Technologies Group]**

**08/2014 - 07/2017**

R&D Electronic Engineer | Santander

Tedesys is a company with different services, for example, MPSoC (Multiprocessor System On Chip) platform-based design, integration HW/SW, implementation on FPGA/ASIC design, printed circuit design, embedded SW development, operating systems and drivers, video processing and video processing architectures for 3D.

I joined Tedesys in 2014 working as a project manager. I was in charge of different kind of projects, being in charge of HW design: electronic system design, schematics and PCB design, as well as being in charge of embedded design systems based on Linux and real-time operative systems based on FreeRTOS.

## **Vista Silicon**

**04/2008 - 06/2013**

Electronic Engineer | Santander

Vista Silicon is a technology-based company, specialized in video and audio processing.

I joined Vista Silicon in 2008 working as an electronic engineer. I was in charge of the following tasks: Management for the electronic design area, design of electronic platforms based on FPGAs (Altera) and microprocessors (Freescale i.MX27 and IT OMAP3 - Beagleboard) and management of manufacturing processes and testing, in prototypes and series.

## **Trasmelec**

**11/2003 - 03/2008**

Automation/Electronic Engineer | Heras

Trasmelec is an electrical engineering company expert in automation processes, industrial instrumentation and industrial electrical installations.

I joined Trasmelec in 2003 working as an automation/electronic engineer. I was in charge of the following tasks: Coordination and supervision of projects, development of automation software and instrumentation systems, and development of electronic applications with microcontrollers.

## **Instituto de Física de Cantabria [IFCA]**

**01/2002 - 10/2003**

Intern I3PG | Santander

Postgraduate Scholar I3PG CSIC (Superior Council of Scientific Research), for the development of the research: "Analysis of the systematic effects in the subsequent modules of amplification at 30 and 44 GHz.", framed within the project of the ESA: "Radiometers of the Planck Mission at 30 and 44 GHz".

## **CSIC [Consejo Superior de Investigaciones Científicas]**

**06/2001 - 12/2001**

HW/SW Engineer | Santander

Development of software and hardware of a data acquisition system. Contracted by the CSIC, a project included in the Planck Mission of the ESA (European Space Agency): "Radiometers of the Planck Mission at 30 and 44 GHz" (PNE-025/2001-C-01).

## **EDUCATION**

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### **Electronic Engineer**

**2003 - 2006**

University of the Basque Country.

### **Degree in Physical Sciences**

**1994 - 2000**

Electronics Speciality: Radio Frequency (2002) and Microprocessors (2000).

University of Cantabria.

## **LANGUAGES**

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### **EOI Santander: C1.1 Level**

**06/2020**

EOI Santander: C1.1 Level.

### **Advanced English Certificate**

**09/2017**

EOI Santander: B2 Level.