



ÓSCAR GÓMEZ FUENTE

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PROFESSIONAL PROFILE

I hold a Bachelor's degree in Physics and an Engineering degree in Electronics, with over 20 years of experience in the electronics sector. Throughout my career, I have led numerous projects and managed teams, consolidating my expertise in electronics.

My experience includes hardware design and development, working with a wide range of platforms, microprocessors, microcontrollers, and communication modules. I am proficient in the use of lab equipment, including oscilloscopes, power supplies, signal generators, DC power analyzers, and vector network analyzers.

I have used different hardware design tools, but for the past 15 years, I have been working with Altium Designer. I have specialized in electronic design techniques for compliance with EMC (Electromagnetic Compatibility), with the goal of obtaining the CE Marking.

I have advanced skills in hardware design tools, such as Altium Designer. My specialization in electronic design techniques ensures compliance with Electromagnetic Compatibility (EMC) standards, facilitating the achievement of CE marking for designed products.

Additionally, I have significant experience in the development of optimized and customized embedded platforms based on buildroot, as well as the development of embedded platforms based on microcontrollers with Real-Time Operating Systems (RTOS) such as FreeRTOS.

In the last 7 years, I have focused on the development of IoT (Internet of Things) products, utilizing LPWAN (Low Power Wide Area Network) technologies such as Sigfox, LoRa, NB-IoT, and LTE-M. I have led the design of multiple products, managing the entire process from firmware and electronic design to enclosure design, antenna adaptation, and obtaining the necessary certifications for commercialization.

For more information, visit my website: www.oscargomezf.com.

PROFESSIONAL EXPERIENCE

TST Sistemas [Celestia Technologies Group]

03/2021 – Now

Director of Engineering | Santander

In this new role at TST, I lead a multidisciplinary team of 8 engineers, coordinating efforts for the development and execution of engineering projects, IoT product development, as well as the preparation and presentation of proposals for tenders.

I oversee the entire lifecycle of technical projects, from conceptualization and planning to execution and delivery, ensuring deadlines, budgets, and quality standards are met, while managing risks and solving problems proactively.

Additionally, I have implemented various procedures, such as document management and storage, design change requests, repository management, antenna adaptation, radiation pattern generation, etc.

TST Sistemas [Celestia Technologies Group]

03/2018 – 02/2021

IoT Project manager | Santander

I led the development of IoT projects, involving myself in all key phases, from conceptualization to implementation. This included both hardware design and firmware development for embedded systems, ensuring efficient and robust solutions that met all technical and functional project requirements.

Celestia Technologies Group
R&D Electronic Engineer | Santander

08/2017 - 02/2018

After Tedesys Global was acquired by Celestia Group, I continued with the pending tasks in R&D projects.

Tedesys Global [Celestia Technologies Group]
R&D Electronic Engineer | Santander

08/2014 - 08/2017

At Tedesys, a company specializing in advanced technological solutions, I led and managed various R&D projects, focusing on MPSoC system design, HW/SW integration, and embedded systems development. My main responsibilities included:

- Hardware design, schematics, and PCB layout, ensuring the quality and optimization of electronic systems.
- Development of embedded systems on Linux platforms and real-time operating systems (FreeRTOS).
- Technical coordination of projects, ensuring efficient integration between hardware and software, as well as meeting deadlines and technical requirements.

Vista Silicon
Electronic Engineer | Santander

04/2008 - 06/2013

At Vista Silicon, a company specializing in audio and video processing, I was responsible for the design and development of electronic systems based on advanced technologies, including FPGAs (Altera) and microprocessors (Freescale i.MX27, TI OMAP3, Beagleboard). My responsibilities included:

- Design and validation of electronic systems for audio and video applications.
- Development of procedures for pre-series testing and optimization of the production process.
- Implementation of efficient solutions to ensure quality in manufacturing and device performance.
- Close collaboration with the production team to establish standards and procedures that improved efficiency and final product quality.

Trasmelec
Automation/Electronic Engineer | Heras

11/2003 - 03/2008

At Trasmelec, a company specializing in industrial automation and instrumentation, I held key responsibilities in coordinating and supervising electrical engineering projects. My main duties included:

- Coordination and supervision of industrial automation and instrumentation projects, ensuring the efficient implementation of complex electrical systems.
- Software development for automation systems, improving industrial processes through customized solutions.
- Design and development of electronic applications based on microcontrollers, optimizing control and monitoring of industrial systems.

Instituto de Física de Cantabria [IFCA]
Intern I3PG | Santander

01/2002 - 10/2003

Postgraduate Fellow I3PG CSIC (Spanish National Research Council), working on the research: "Analysis of systematic effects in the posterior amplification modules at 30 and 44 GHz," within the framework of the ESA project: "Radiometers of the Planck Mission at 30 and 44 GHz."

CSIC [Consejo Superior de Investigaciones Científicas]
HW/SW Engineer | Santander

06/2001 - 12/2001

Development of the software and hardware for a data acquisition system. Hired by the CSIC, for a project within the framework of the ESA Planck Mission: "Radiometers of the Planck Mission at 30 and 44 GHz" (PNE-025/2001-C-01).

LANGUAGES

Advanced English Certificate

EOI Santander: B2 Level.

09/2017

EDUCATION

Master's in Industry 4.0

PEAKS Business School.

2021 - 2022

Electronics Engineer

University of the Basque Country.

2003 - 2006

Electronics Engineer

Specialized in Electronics: RF Microwaves (2002) and Microprocessors (2000).
University of Cantabria.

1994 - 2000